

Lab 3

Tyler Korz - CMPEN 331 Sec 002



1. Verilog Code located in File Provided
2. Testbench code located in File Provided

A screenshot of a computer

Description automatically generated

1. A computer screen shot of a diagram

   Description automatically generated
2. A screenshot of a computer game

   Description automatically generated
3. A screen shot of a computer

   Description automatically generated